



TITLE:

# 11. Electrical Properties and Auger Analysis of Al<sub>2</sub>O<sub>3</sub>-Native Oxide-InP Interfaces

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CITATION:

福田, 充広. 11. Electrical Properties and Auger Analysis of Al<sub>2</sub>O<sub>3</sub>-Native Oxide-InP Interfaces. 物性研究 1986, 46(5): 763-763

ISSUE DATE:

1986-08-20

URL:

<http://hdl.handle.net/2433/92212>

RIGHT:

## 10. $\text{Fe}_x\text{Co}_{1-x}\text{Si}$ の磁化と強制体積磁歪

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遍歴電子強磁性体の一つである  $\text{Fe}_x\text{Co}_{1-x}\text{Si}$  について、磁化測定及び強制体積磁歪の測定を行なったところ、

- (1) 自発磁化の温度変化は Stoner モデルより SCR 理論の方がよい記述を与える、
  - (2) キュリー温度以上の強制体積磁歪は磁化の自乗にほぼ比例し、
- 大きな磁気体積結合係数が得られた。

## 11. Electrical Properties and Auger Analysis of $\text{Al}_2\text{O}_3$ -Native Oxide-InP Interfaces

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The interfacial properties of both Al- $\text{Al}_2\text{O}_3$ -native oxide-InP MIS diodes and Al- $\text{Al}_2\text{O}_3$ -InP MIS ones are investigated by using capacitance-voltage method and Auger electron spectroscopy. Native oxide is fabricated intentionally by employing the oxidation process with  $\text{HNO}_3$  at  $75^\circ\text{C}$  under illumination and the  $\text{Al}_2\text{O}_3$  film is obtained by electron beam evaporation of sapphire. The pinning position of surface Fermi level for the diode with intentional native oxide is different from that for the diode without intentional native oxide. In the former diode, phosphorus penetrates deep inside the oxide layer and in the latter, the indium deficient region exists just adjacent to InP bulk. The origin of surface states which cause the difference in Fermi-level pinning position is discussed on the basis of the results of Auger Analysis.